

Applicant(s): T. Berg et al.

Serial No.: 10/045,925

Filed: January 9, 2002

Title: DISTRIBUTED ALLOCATION OF SYSTEM

HARDWARE RESOURCES FOR MULTIPROCESSOR SYSTEMS

Attorney Docket No.: BEA920000022US1

Group Art Unit:

2151

Examiner:

Unassigned

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MAY 0 8 2002

Technology Center 2100

Box DD

Assistant Commissioner for Patents

Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

This Information Disclosure Statement is submitted under 37 CFR 1.97(b).

Applicant(s) submit herewith Form PTO SB08A-Information Disclosure Statement By Applicant together with copies, of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

The relevance of the attached references is that this is the closest art of which Applicant is aware. Applicant submits that the above references taken alone or in combination neither anticipate nor render obvious the present invention. Consideration of the foregoing in relation to this application is respectfully requested.

It is requested that the information disclosed herein be made of record in this application.

1

IBM Corporation

Intellectual Property Law Dept., EDO2-06

15450 SW Koll Parkway Beaverton, OR 97006-6063 (503) 578-5020 voice

(503) 578-5040 fax

Respectfully submitted,

Pryor A Garnet

Attorney for Applicant

Registration No. 32,136

Information Disclosure Statement March 20, 2002

Serial No.: 10/045,925

Docket: BEA920000022US1



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Technology Center 2100 DISTRIBUTED ALLOCATION OF SYSTEM HARDWARE

RESOURCES FOR MULTIPROCESSOR SYSTEMS

Box DD

Assistant Commissioner for Patents

Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL LETTER

Enclosed herewith is a Information Disclosure Statement pursuant to 37 CFR. § 1.56 in connection with the above-identified application, which statement is being filed:

- [] Together with the present application.
- [X]Before the first Office Action on the merits or three (3) months from the filing date of this application, whichever occurs last. [37 CFR § 1.97(b)]
- After the first Office Action on the merits, but before a Final Office Action under §1.113 or Notice of Allowance under §1.311, whichever occurs first. [37 CFR § 1.97(c)
- After a Final Office Action under §1.113 or Notice of Allowance under §1.311, but prior to or with payment of the Issue Fee. [37 CFR § 1.97(d)]

Consistent with Applicant's obligations pursuant to 37 CFR §§1.97 and 1.98, the following requirements have been met:

- [X]No separate requirements are needed.
- [X]No additional fee is required.

Docket: BEA920000022US1 Serial No.: 10/045,925

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IDS Transmittal May 1, 2002

Provision of Copies of References

- [X] Copies of all cited references are enclosed.
- [] A copy of the ABSTRACT of each of the references listed on the attached Form PTO-SB08A is enclosed herewith and forms a part hereof.
- [] A partial translation of Reference _____ is enclosed herewith and forms a part hereof.
- [] A copy of the European Search Report from a corresponding or related EPO application is enclosed herewith.
- [] A copy of the International Search Report from a corresponding or related PCT application is enclosed herewith.
- [X] The Commissioner is hereby authorized to charge any fees associated with this communication or credit any overpayment to Deposit Account No. <u>501336</u>. A duplicate copy of this transmittal is attached.

Respectfully submitted,

Date: 1 1 ay 2002

Pryor A. Garnett Registration No. 32,136

IBM Corporation Intellectual Property Law Dept., EDO2-06 15450 SW Koll Parkway Beaverton, OR 97006-6063

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PTO/SB/08A (10-01)
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U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

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Co	mplete if Known	1
Application Number	10/045,925	
Filing Date	January 9, 2002	
First Named Inventor	T. Berg et al.	
Art Unit	2151	
Examiner Name	Unassigned	
Attorney Docket Number	BEA920000022US1	

			U.S. PAT	ENT DOCUMENTS		
Examiner Initials		Document Number Number - Kind Code ² (<i>if known</i>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	1	US- 5,974,456	10-26-1999	Naghshineh et al.	PEO	EIVEL
***************************************	2	us- 5,930,484	07-27-1999	Tran et al.	"""	EIVER
	3	US- 5,761,413	06-02-1998	Frank et al.	1	
	4	∪s- 5,652,885	07-29-1997	Reed et al.	MAY	8 2002
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	8	us- 5,161,227	11-03-1992	Dias et al.		
	9	us- 5,027,271	06-25-1991	Curley et al.		
	10	us- 4,980,822	12-25-1990	Brantley, Jr. et al.		
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

Complete if Known

Application Number | 10/045,925

Filing Date | January 9, 2002

First Named Inventor | T. Berg et al.

Examiner Cite	OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue	T ²
Initials No.1	number(s), publisher, city and/or country where published	
11	GARCIA, A., "Tagged Inter Processor Communication Bus for Multiprocessor Systems," IBM Technical Disclosure Bulletin, August 1991, pp. 115-121, Vol. 34, No. 3.	
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Examiner	Date	

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